memory of said computer.

60.   A true random number generator as in claim 59 wherein said means for interfacing comprises a device driver.

61.   A true random number generator as in claim 59 wherein said means for interfacing comprises software for testing said true random number generator circuit.

62.   A true random number generator as in claim 59 wherein said true random number generator circuit is located in a module separate from said computer, and said true random number generator further includes a cable for electrically connecting said module to said computer.

63.   A true random number generator as in claim 59 wherein said true random number generator circuit is located on an add-on board for mounting in said computer.

64.   A true random number generator as in claim 59 wherein said true random number generator circuit is located on the motherboard of said computer.

65.   A true random number generator as in claim 59 wherein said true random number generator circuit is located on a peripheral of said computer.

66.   A device for interfacing with a true random number generator, said device comprising:

a computer including: memory means for storing information for interfacing with a true random number generator circuit, and processing means communicating with said memory for interfacing with said true random number generator; and wherein

said information for interfacing with a true random number generator consists of one or more of the following: a device driver, a TSR, a portion of the operating system of said computer, and a program stored in the bios memory of said computer.

REMARKS

The examiner has required a restriction between the following Groups of claims: I: Claims 8 – 14 and 18; II: Claims 15 – 17 and 19; III: Claim 20; IV: Claims 29 – 34; V: Claims 35 – 39; and VI: Claims 40 – 58. Please note that the examiner places claim 40 in Group V, but it is believed that it belongs in Group VI since it is drawn to a true random number generator and not a method of design and fabricating a random number generator.

Doc. No. 4385
U.S. Patent Application Serial No.: 09/699,523
Response to Office Action Mailed 01/11/02
Page 2